

# ***LIQUID CRYSTAL DISPLAY DRIVER AND METHOD THEREOF***

## **Abstract of Disclosure**

JP920010105US125 A liquid crystal display driver for applying a voltage to liquid crystal cells forming an image display area includes a pulse generation circuit for generating a plurality of reference pulses in which pulse generation densities are weighted, a pulse select/synthesis circuit for generating a pulse string by selecting and synthesizing necessary reference pulses on the basis of digital input data and the reference pulses and an integration circuit (low pass filter) for integrating the pulse string generated by the pulse select/synthesis circuit to output an analog voltage for gamma correction.

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## Figures

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